

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): En-Sing Chen, et al.

Title: NAND MEMORY ARRAY INCORPORATING CAPACITANCE
BOOSTING OF CHANNEL REGIONS IN UNSELECTED MEMORY
CELLS AND METHOD FOR OPERATION OF SAME

Application No.: 10/729,831

Filed: December 5, 2003

Examiner: Le, Toan K.

Group Art Unit: 2824

Atty. Docket No.: 023-0028

Confirmation No.: 8528

March 15, 2005

Mail Stop Amendment
COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

37 C.F.R. § 1.97(b)

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, the undersigned brings to the Examiner's attention in the above-identified application the patents, publications, applications or other information identified in the attached:

- ☒ Form(s) PTO/SB/08A (5 page(s), with copy of 4 reference(s)).
- ☒ Form(s) PTO/SB/08B (2 page(s), with copy of 20 reference(s)).
- ☒ Listing of Pending Applications (1 page(s), with copy of no application(s)).
- ☐ Other: N/A

Citation of such information shall not be construed as (i) an admission that the information necessarily is, or corresponds to, prior art with respect to the instant invention, (ii) a representation that a search has been made, other than as described herein, or (iii) an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

For each item of information listed that is not in the English language, the undersigned has provided a concise explanation of the relevance through (i) an English language abstract, (ii) an English language equivalent application, or (iii) if cited in a search report or other action by a foreign patent office in a counterpart foreign application, an English language version of the search report or action that indicates the degree of relevance found by the foreign office.

CERTAIN COPIES NOT INCLUDED

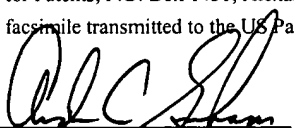
Pursuant to operative regulations, certain copies need not be provided and corresponding information is identified in this Information Disclosure Statement by citation only. In particular,

- ☒ In accordance with 37 C.F.R. § 1.98(a)(2)(ii), copies of cited U.S. Patents and U.S. Patent Application Publications are not included.
- ☐ Certain information was submitted to, or cited by, the Office in Application No(s). N/A, filed N/A, to which the above-identified application claims priority under 35 U.S.C. § 120. In accordance with 37 C.F.R. § 1.98(d), copies of that information are not included.
- ☒ Pursuant to 1287 OG 163 (October 19, 2004), for those cited pending U.S. patent applications filed (or which entered the national stage under 35 U.S.C. § 371) on or after June 30, 2003 or which are otherwise stored in the USPTO's Image File Wrapper system, copies are not included.

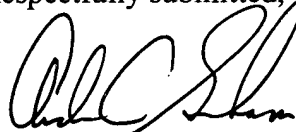
FEE AUTHORIZATION

- ☐ This Information Disclosure Statement is filed within three months of the filing date of a national application other than a continued prosecution application under § 1.53(d) or within three months of entry of the national stage as set forth in § 1.491 in an international application. Therefore, no fee is required.
- ☒ The undersigned believes that this Information Disclosure Statement is being filed before the mailing date of a first Office action on the merits or before the mailing date of a first Office action after the filing of a request for continued examination under § 1.114. Therefore, no fee is believed required.

If however, this Information Disclosure Statement is filed after the period specified in § 1.97(b), the undersigned hereby authorizes the Commissioner to charge the fee set forth in § 1.17(p) to Deposit Account No. 50-0631.

| <u>CERTIFICATE OF MAILING OR TRANSMISSION</u> | |
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|  Andrew C. Graham | <u>3-15-05</u> Date |

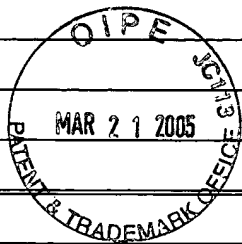
Respectfully submitted,



Andrew C. Graham, Reg. No. 36,531
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EXPRESS MAIL LABEL: _____

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| PTO/SB/08A (Substitute for form 1449/PTO) | | Attorney Docket No.: 023-0028 |
| | | Application No.: 10/729,831 |
| INFORMATION DISCLOSURE STATEMENT BY APPLICANT | | First Named Inventor: En-Hsing Chen et al. |
| | | Filing Date: December 5, 2003 |
| | | Group Art Unit: 2824 |
| | | Examiner Name: Toan K. Le |
| Sheet 1 of 5 | | Date Submitted: March 15, 2005 |



| U.S. PATENT DOCUMENTS | | | | | | |
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| Examiner Initials* | Cite No. ¹ | Document Number Number-Kind Code ² (if known) | Publication Date MM-DD-YYYY | Name of Patentee or Applicant of Cited Document | Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear | |
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| Examiner Signature | | Examiner Signature | | Date Considered | | |

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Applicant's unique citation designation number (optional).

²See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04.

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⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document.

⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible.

⁶Applicant is to place a check mark here if English language Translation is attached.

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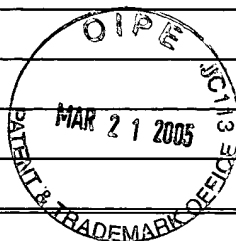
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| | BE | | | | | |
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| Sheet 5 of 5 | | Date Submitted: March 15, 2005 |

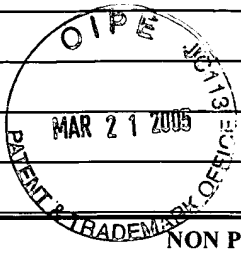
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| Sheet 1 of 2 | | Date Submitted: March 15, 2005 | |



| NON PATENT LITERATURE DOCUMENTS | | | |
|---------------------------------|-----------------------|--|----------------|
| Examiner Initials* | Cite No. ¹ | Include name of author (in CAPITAL LETTERS), title of article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T ² |
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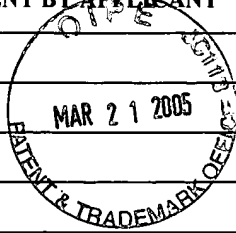
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|--------------------|-----------------------|---|----------------|
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LIST OF PENDING U. S. PATENT APPLICATIONS

The following pending U.S. patent applications are listed for consideration by the Examiner.

- 1) U.S. Patent Application No. 10/729,865, filed December 5, 2003, entitled "NAND Memory Array Incorporating Multiple Series Selection Devices and Method for Operation of Same," naming inventors En-Hsing Chen, Andrew J. Walker, Roy E. Scheuerlein, Sucheta Nallamothe, Alper Ilkbahar, Luca G. Fasoli and James M. Cleeves.
- 2) U.S. Patent Application No. 10/729,844, filed December 5, 2003, entitled "NAND Memory Array Incorporating Multiple Write Pulse Programming of Individual Memory Cells and Method for Operation of Same," naming inventors En-Hsing Chen, Andrew J. Walker, Roy E. Scheuerlein, Sucheta Nallamothe, Alper Ilkbahar, and Luca G. Fasoli.
- 3) U.S. Patent Application No. 10/729,843, filed December 5, 2003, entitled "Memory Array Incorporating Memory Cells Arranged in NAND Strings," naming inventors Luca G. Fasoli, Roy E. Scheuerlein, En-Hsing Chen, Sucheta Nallamothe, Maitreyee Mahajani, and Andrew J. Walker.

Applicant(s) are providing a separate list of pending applications, pursuant to MPEP § 609III(D), to avoid publication of the application numbers on any patent that issues from the above-identified application. Applicant(s) request that the Examiner indicate consideration of the pending applications listed above in accordance with MPEP 609III(C)(2) and return a copy of this page with the next communication.